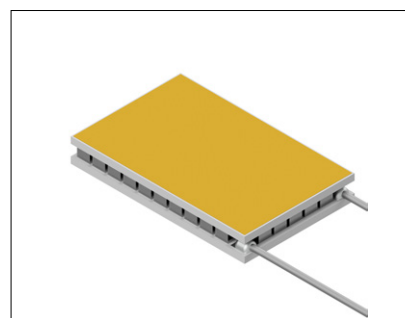


## Performance Parameters

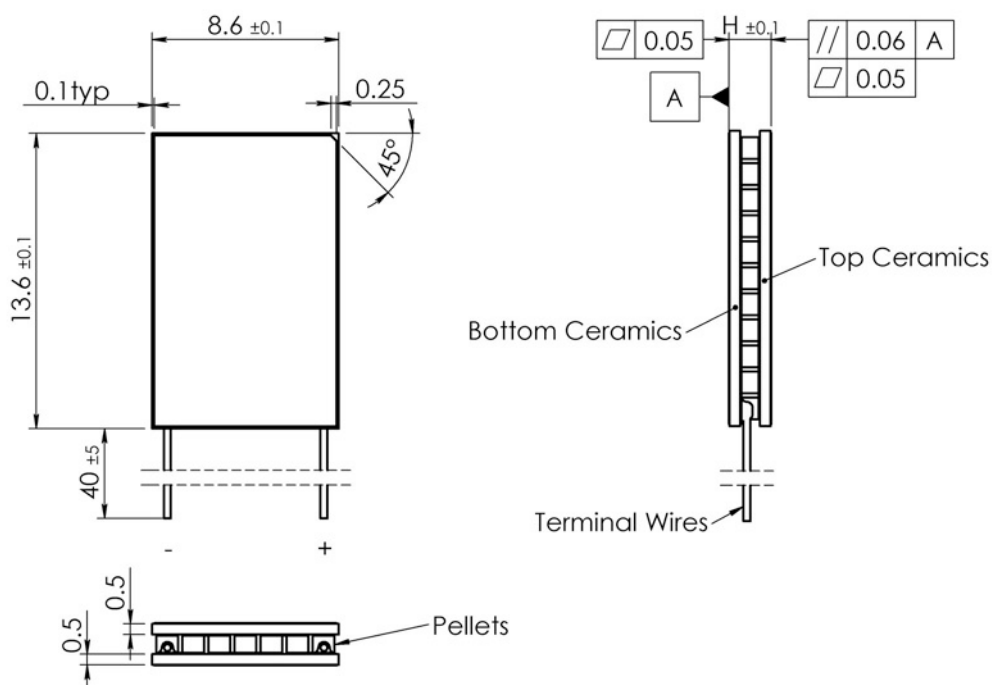
1MD10-037-XX/Z2

| Type                   | $\Delta T_{\max}$<br>K | $Q_{\max}$<br>W | $I_{\max}$<br>A | $U_{\max}$<br>V | AC R<br>Ohm | H<br>mm |
|------------------------|------------------------|-----------------|-----------------|-----------------|-------------|---------|
| 1MD10-037-XX/Z2 (N=37) |                        |                 |                 |                 |             |         |
| 1MD10-037-08/Z2        | 72                     | 16.3            | 5.6             | 4.8             | 0.64        | 1.9     |
| 1MD10-037-10/Z2        | 72                     | 13.3            | 4.5             |                 | 0.79        | 2.1     |
| 1MD10-037-15/Z2        | 74                     | 9.1             | 3.1             |                 | 1.16        | 2.6     |
| 1MD10-037-20/Z2        | 74                     | 6.9             | 2.3             |                 | 1.52        | 3.1     |

Performance data is given for 300K, vacuum



## Dimensions



## Available options

### A. TEC Assembly:

- \* 1. Solder SnSb ( $T_{\text{melt}}=230^{\circ}\text{C}$ )
- 2. Solder AuSn ( $T_{\text{melt}}=280^{\circ}\text{C}$ )

### B. Ceramics

- \* 1. Aluminum Nitride (AlN)
- 2. Pure  $\text{Al}_2\text{O}_3$  (100%)
- 3. Alumina ( $\text{Al}_2\text{O}_3$ - 96%)

\* - used by default

### C. Ceramics Surface Options:

- 1. Blank ceramics (not metallized)
- 2. Metallized (Au plating)
- 3. Metallized and pre-tinned with:
  - 3.1 Solder 117 (In-Sn,  $T_{\text{melt}} = 117^{\circ}\text{C}$ )
  - 3.2 Solder 138 (Sn-Bi,  $T_{\text{melt}} = 138^{\circ}\text{C}$ )
  - 3.3 Solder 143 (In-Ag,  $T_{\text{melt}} = 143^{\circ}\text{C}$ )
  - 3.4 Solder 157 (In,  $T_{\text{melt}} = 157^{\circ}\text{C}$ )
  - 3.5 Solder 183 (Pb-Sn,  $T_{\text{melt}} = 183^{\circ}\text{C}$ )
  - 3.6 Optional (specified by Customer)

### D. Thermistor (optional)

Can be mounted to cold side ceramics edge. Calibration is available by request.

### E. Terminal contacts

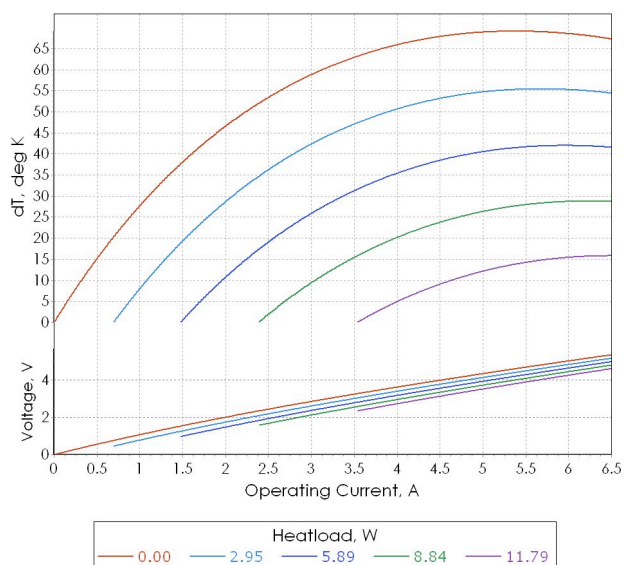
- 1. Blank, tinned Copper
- 2. Insulated Wires
- 3. Insulated, color coded

For more options please consult one of our application engineers.

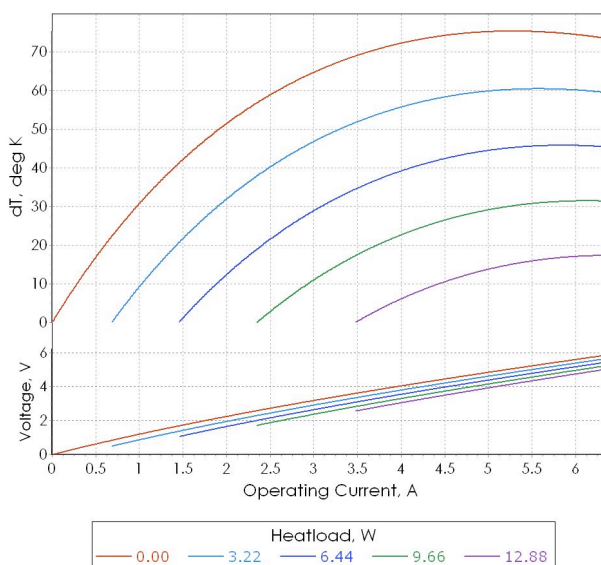
## Performance Curves

1MD10-037-08/Z2

| @ 27°C, Vacuum  | $\Delta T_{max}$<br>K | $Q_{max}$<br>W | $I_{max}$<br>A | $U_{max}$<br>V |
|-----------------|-----------------------|----------------|----------------|----------------|
| 1MD10-037-08/Z2 | 72                    | 16.3           | 5.6            | 4.8            |



| @50°C, N2       | $\Delta T_{max}$<br>K | $Q_{max}$<br>W | $I_{max}$<br>A | $U_{max}$<br>V |
|-----------------|-----------------------|----------------|----------------|----------------|
| 1MD10-037-08/Z2 | 78                    | 18.0           | 5.5            | 5.3            |

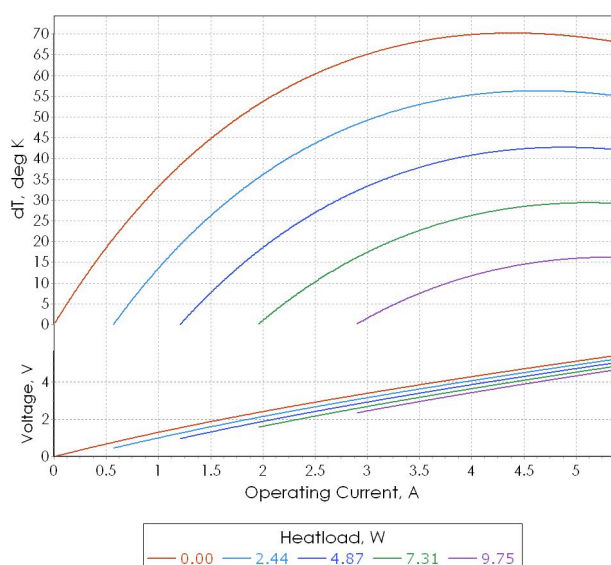


**Note:** Performance curves are specified for optimal conditions where the TEC hot side is stabilized at ambient temperature. Module temperature does not include thermal resistance of heat sinks. For performance information in alternative environments, temperature ranges or specific heatsinks, please consult one of our application engineers.

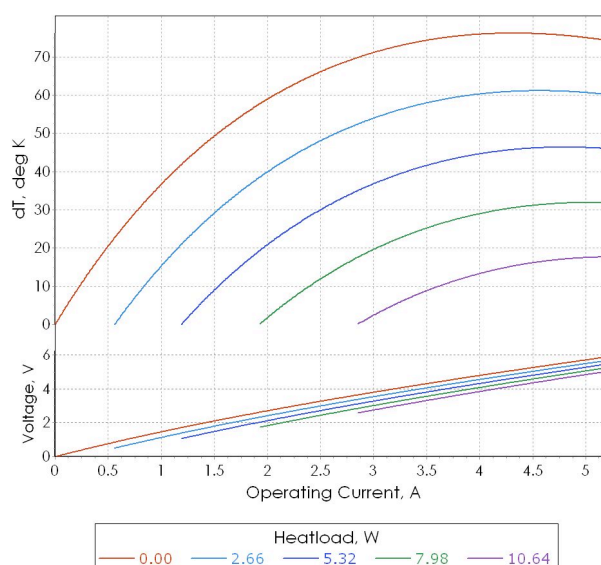
## Performance Curves

1MD10-037-10/Z2

| @ 27°C, Vacuum  | $\Delta T_{max}$<br>K | $Q_{max}$<br>W | $I_{max}$<br>A | $U_{max}$<br>V |
|-----------------|-----------------------|----------------|----------------|----------------|
| 1MD10-037-10/Z2 | 72                    | 13.3           | 4.5            | 4.8            |



| @50°C, N2       | $\Delta T_{max}$<br>K | $Q_{max}$<br>W | $I_{max}$<br>A | $U_{max}$<br>V |
|-----------------|-----------------------|----------------|----------------|----------------|
| 1MD10-037-10/Z2 | 79                    | 14.7           | 4.5            | 5.3            |

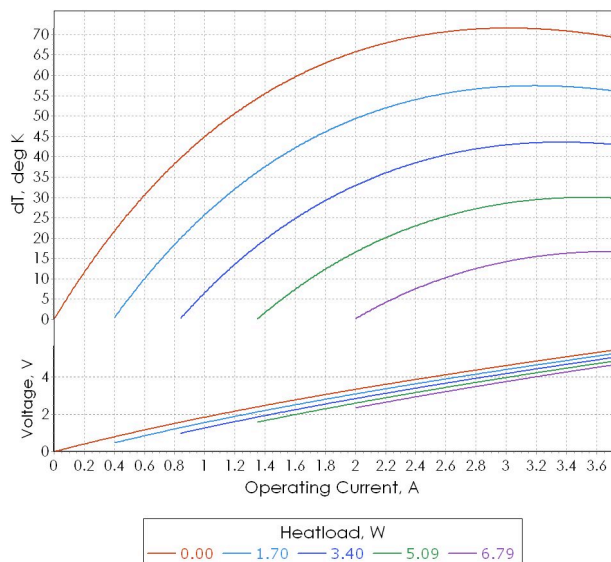


**Note:** Performance curves are specified for optimal conditions where the TEC hot side is stabilized at ambient temperature. Module temperature does not include thermal resistance of heat sinks. For performance information in alternative environments, temperature ranges or specific heatsinks, please consult one of our application engineers.

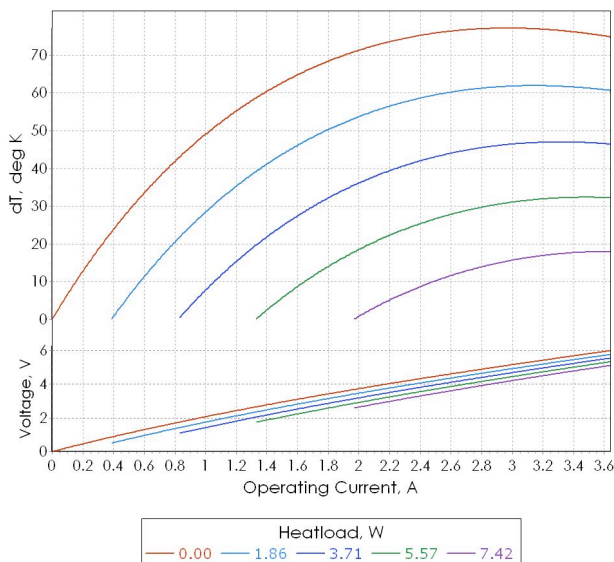
## Performance Curves

1MD10-037-15/Z2

| @ 27°C, Vacuum  | $\Delta T_{\max}$<br>K | $Q_{\max}$<br>W | $I_{\max}$<br>A | $U_{\max}$<br>V |
|-----------------|------------------------|-----------------|-----------------|-----------------|
| 1MD10-037-15/Z2 | 74                     | 9.1             | 3.1             | 4.8             |



| @50°C, N2       | $\Delta T_{\max}$<br>K | $Q_{\max}$<br>W | $I_{\max}$<br>A | $U_{\max}$<br>V |
|-----------------|------------------------|-----------------|-----------------|-----------------|
| 1MD10-037-15/Z2 | 79                     | 10.0            | 3.1             | 5.3             |

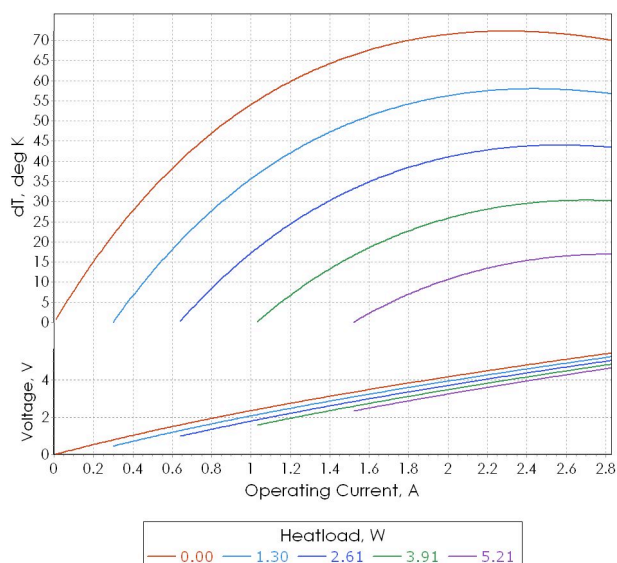


**Note:** Performance curves are specified for optimal conditions where the TEC hot side is stabilized at ambient temperature. Module temperature does not include thermal resistance of heat sinks. For performance information in alternative environments, temperature ranges or specific heatsinks, please consult one of our application engineers.

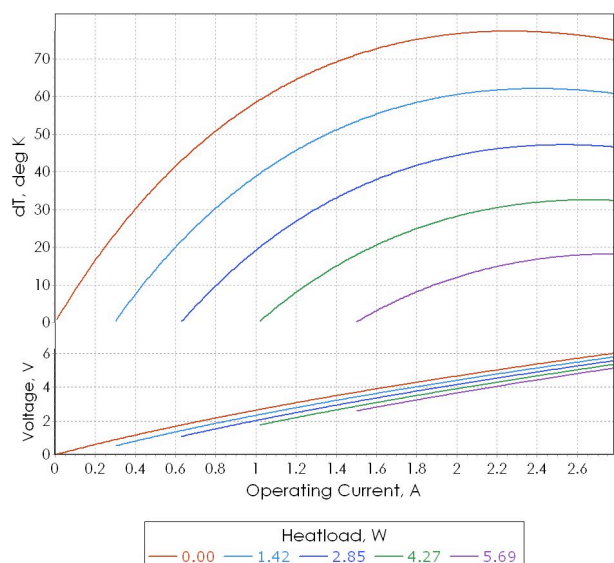
## Performance Curves

1MD10-037-20/Z2

| @ 27°C, Vacuum  | $\Delta T_{\max}$<br>K | $Q_{\max}$<br>W | $I_{\max}$<br>A | $U_{\max}$<br>V |
|-----------------|------------------------|-----------------|-----------------|-----------------|
| 1MD10-037-20/Z2 | 74                     | 6.9             | 2.3             | 4.8             |



| @50°C, N2       | $\Delta T_{\max}$<br>K | $Q_{\max}$<br>W | $I_{\max}$<br>A | $U_{\max}$<br>V |
|-----------------|------------------------|-----------------|-----------------|-----------------|
| 1MD10-037-20/Z2 | 79                     | 7.6             | 2.3             | 5.3             |



**Note:** Performance curves are specified for optimal conditions where the TEC hot side is stabilized at ambient temperature. Module temperature does not include thermal resistance of heat sinks. For performance information in alternative environments, temperature ranges or specific heatsinks, please consult one of our application engineers.

## Important notes

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1. TEC performance in this datasheet is based on two standard conditions: vacuum at +27°C and dry nitrogen at +50°C. Performance varies in other environments.
2. TEC ACR and Umax values are affected by ambient temperature. They differ from datasheet values at other temperatures. ACR and Umax increase as ambient temperature rises.
3. TEC cooling capacity (Qmax) raises with ambient temperature.
4. Thermoelectric coolers have the best performance in the temperature range from near room up to +80..90°C. The performance is lower at temperatures below 0°C. TEC is not suitable to work at cryogenic temperatures.
5. A TEC's maximum performance does not always occur at its I<sub>max</sub> or U<sub>max</sub>. The real optimal mode may depend on operating conditions, temperature difference and heatload. The TEC performs optimally when operated at currents and voltages lower than the maximum values listed in the datasheet.
6. Rigid mounting of TECs to pure copper, aluminum, or nickel, or mounting objects made of these materials on the TEC cold side, is strongly discouraged. Materials with high thermal expansion coefficients (CTE) shorten TEC lifespan or cause damage during mounting, thermal shock, or temperature cycling. If using these materials is unavoidable, use indium-based soft solders or silicon-based glues with high elasticity.
7. KIMT thermoelectric coolers meet Telcordia GR-468 standards. A Reliability Report is available upon request. KIMT guarantees a minimum operating lifetime of 250,000 to 300,000 hours under normal conditions.
8. RoHS & REACH compliant

## Additional Options

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For further details regarding thermoelectronic options, please access the provided link or utilize the QR code.

<https://kimt-tech.com/support/thermoelectric-module-options/>



## Application Tips

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For further details regarding Application Tips, please access the provided link or utilize the QR code.

<https://kimt-tech.com/support/recommendations-for-use/>

